



PATENT  
Docket No. 81788.0020

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#111B  
1-4 way  
5-6-05

In re application of:

Shota Kitamura, Seiji Yamada

Serial No: 09/392,865

Filed: September 9, 1999

For: Nonvolatile Semiconductor Memory  
Device and Its Manufacturing Method

Art Unit: 2811

Examiner: T. Tran

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:  
Assistant Commissioner for Patents  
Washington D.C. 20231, on

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Name

Signature

4/27/2000  
Date

**AMENDMENT**

Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

This is in response to the Office Action dated December 28, 1999, which was paper #7 of the present application. The time period for responding to the outstanding Office Action has been extended to April 28, 2000 by the filing of a petition for one-month extension of the period for response. Please amend the application as follows:

**IN THE SPECIFICATION:**

At page 6, line 29, please delete the word "floating" and replace it with the word --floating--.

At page 9, line 12, please replace "fist-layer" with --first-layer--.

**IN THE CLAIMS:**

Please cancel claims 1-4 without prejudice.

Please amend claims 5 and 7 as follows:

MAY 05 2000